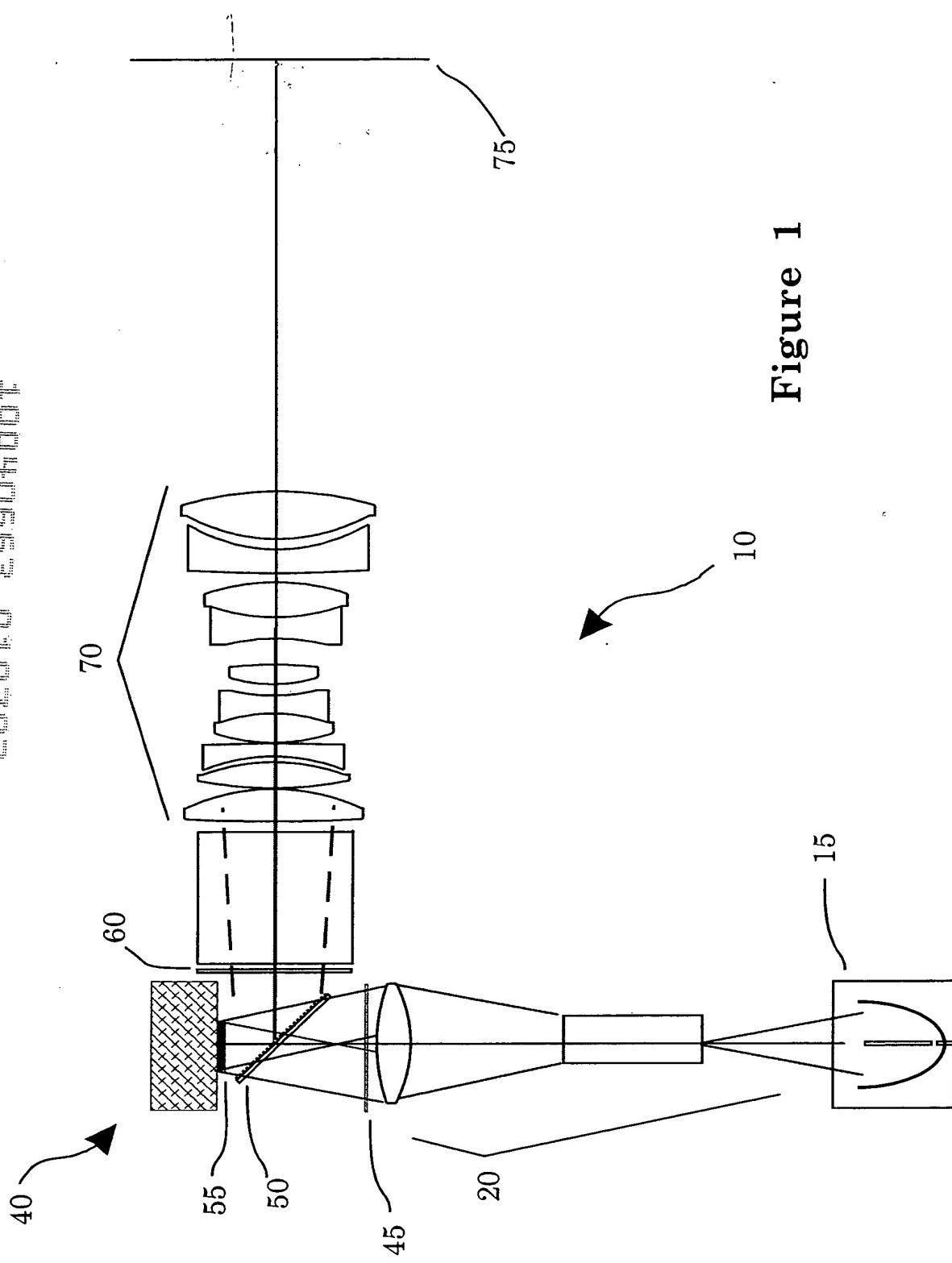
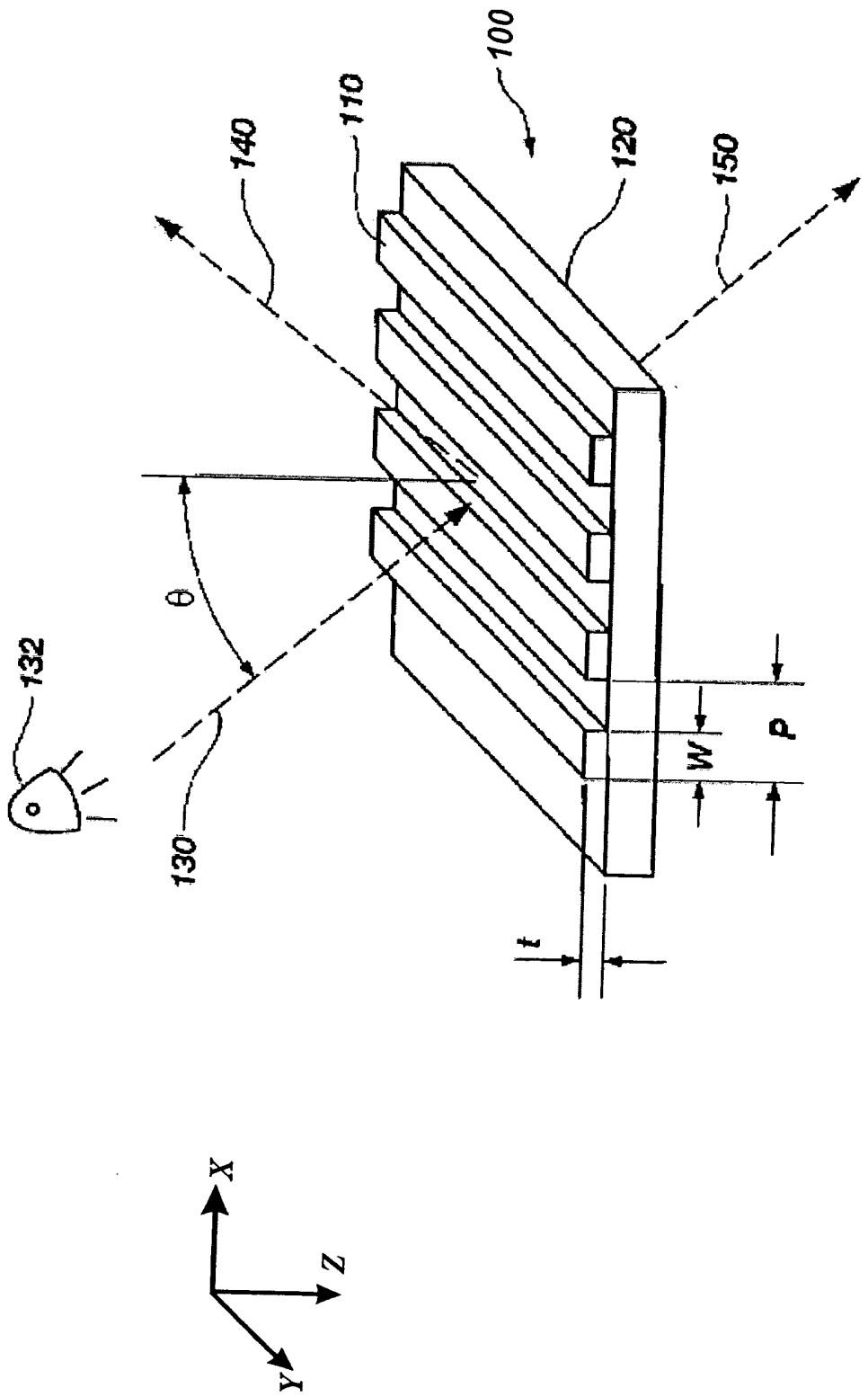


Figure 1





(PRIOR ART)

Figure 2

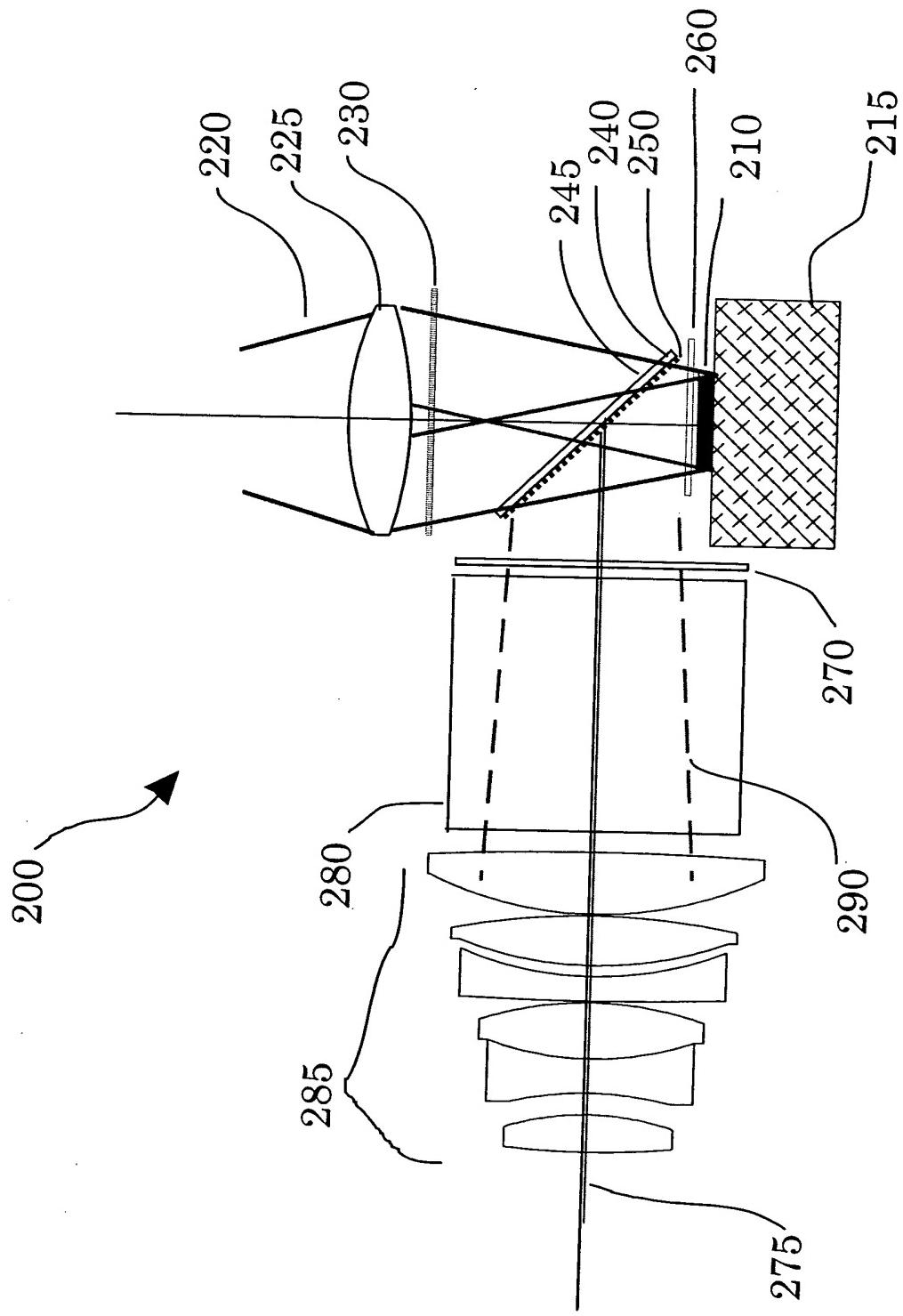


Figure 3

2000 1500 1000 500

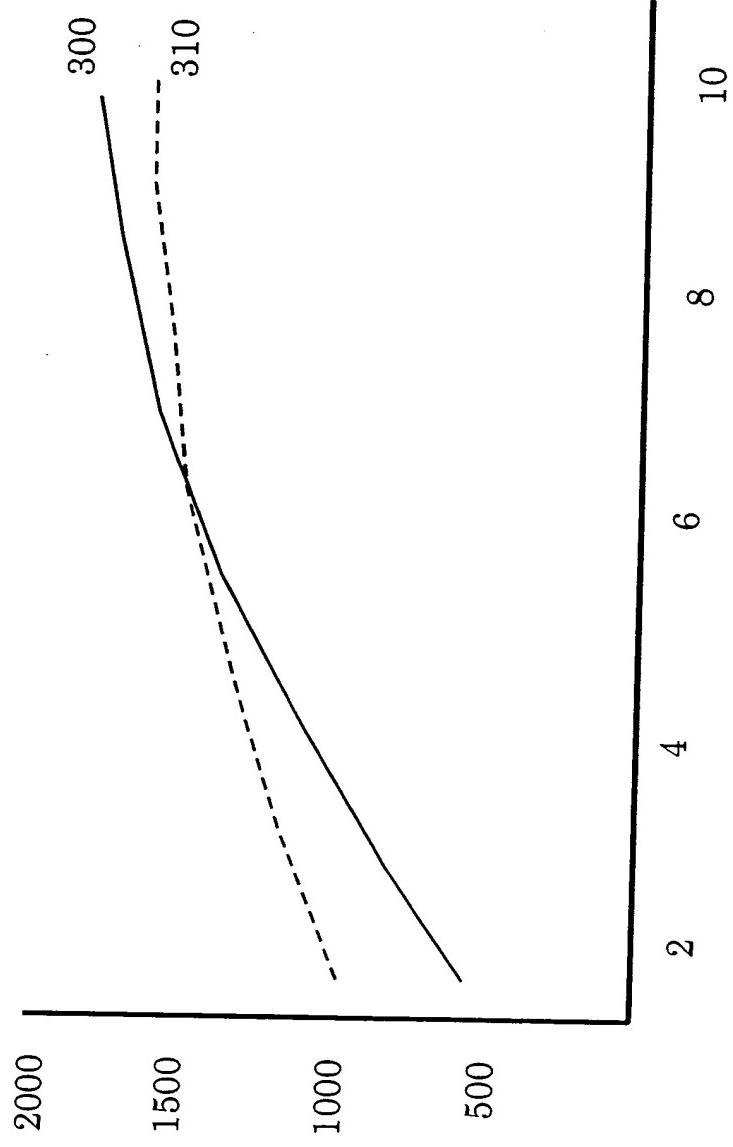
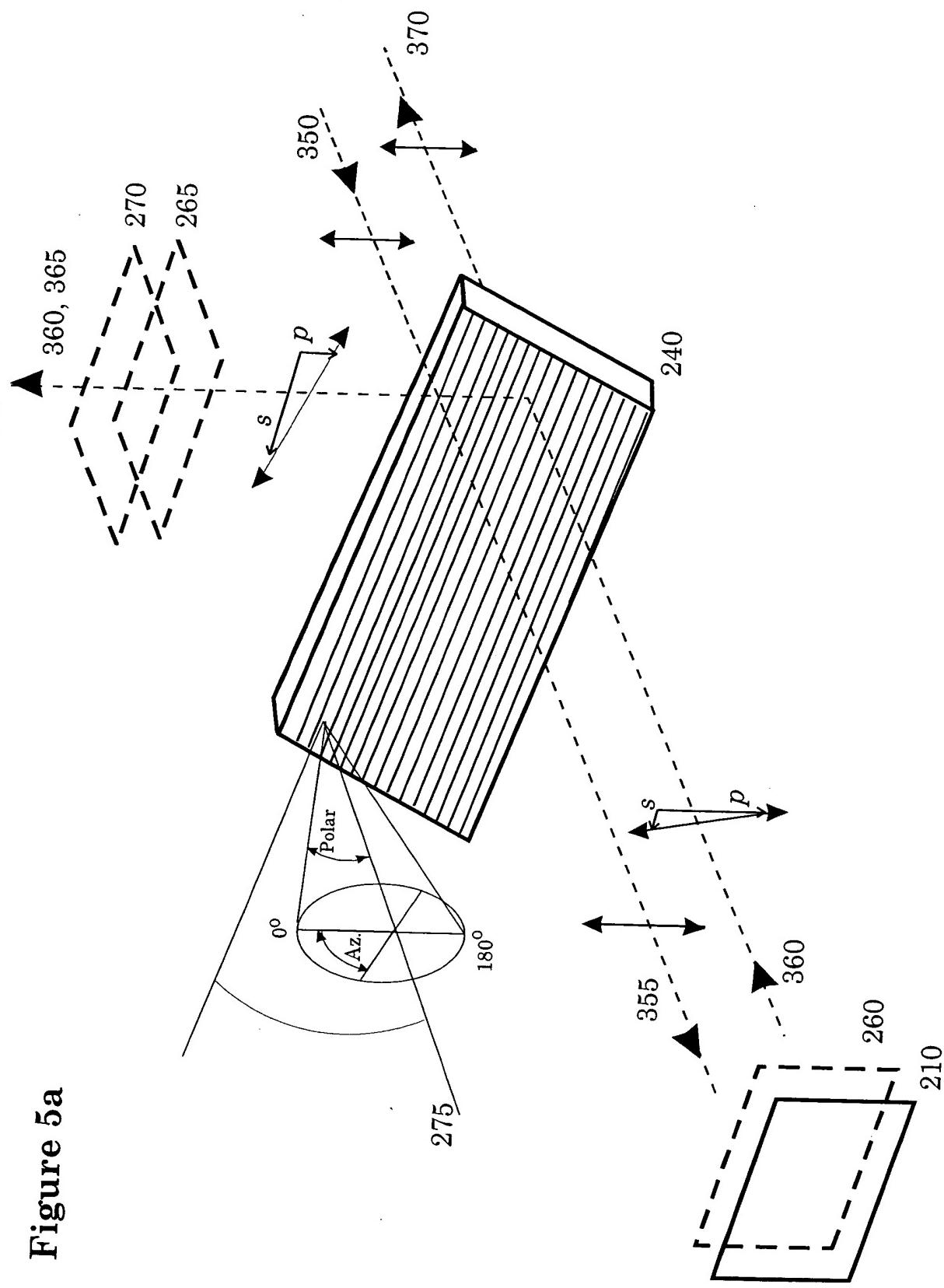


Figure 4

Figure 5a



200 265 210 230

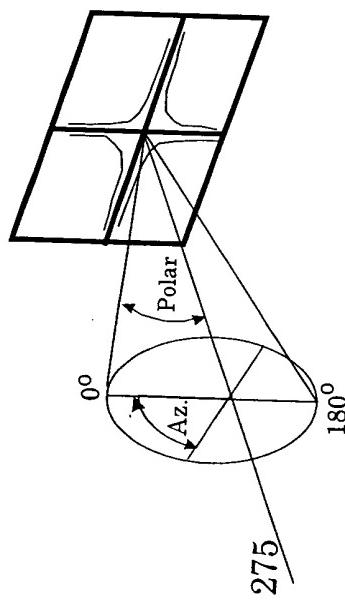
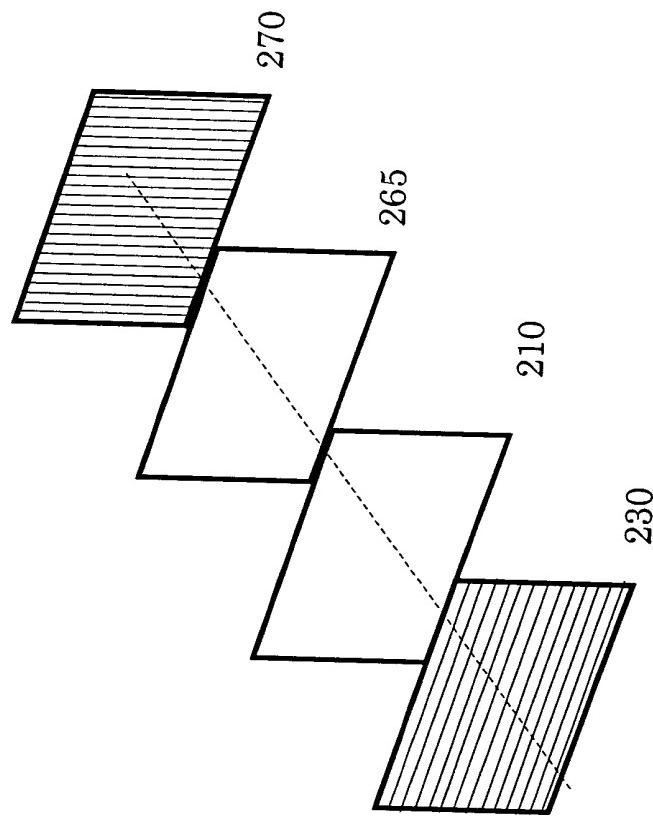
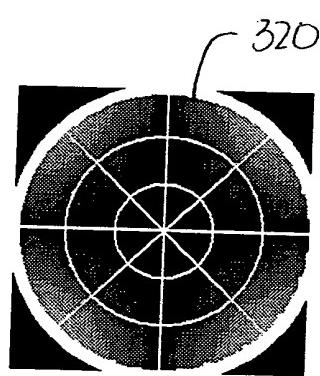


Figure 5b

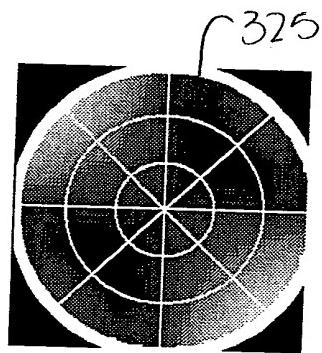
200

Figure 5c





**Figure
6a**



**Figure
6b**

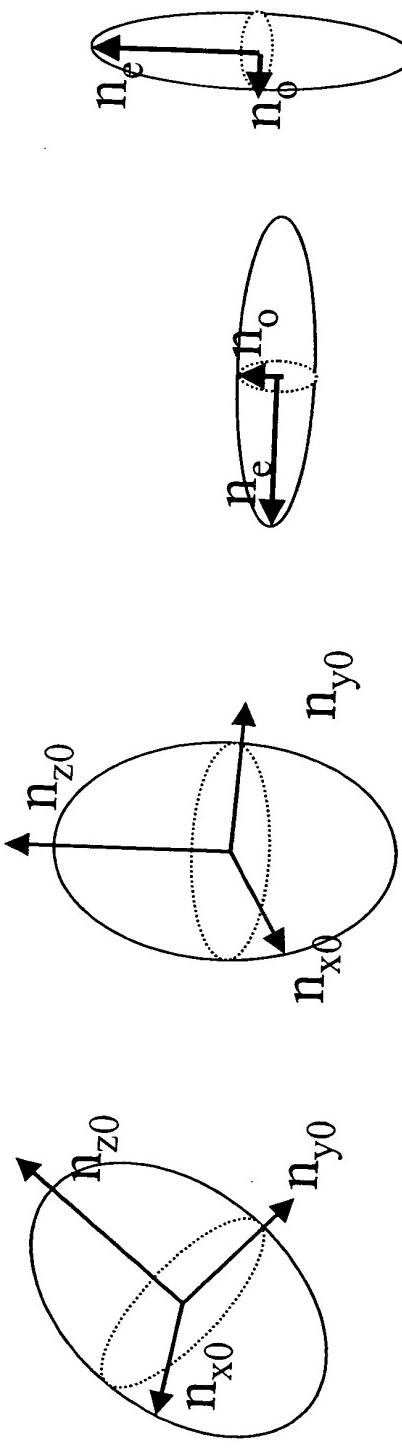


Figure 7a

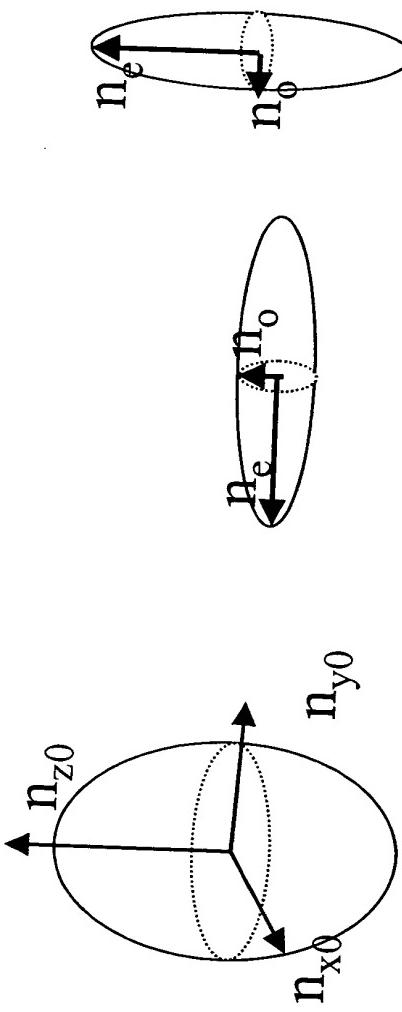


Figure 7b

Figure 7c
Figure 7d

Figure 7d

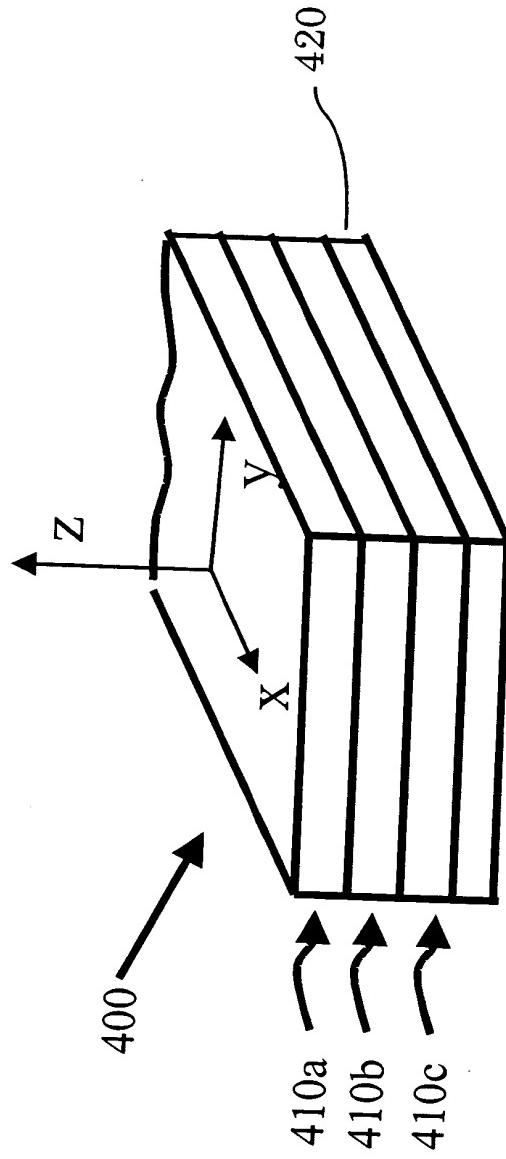


Figure 7e

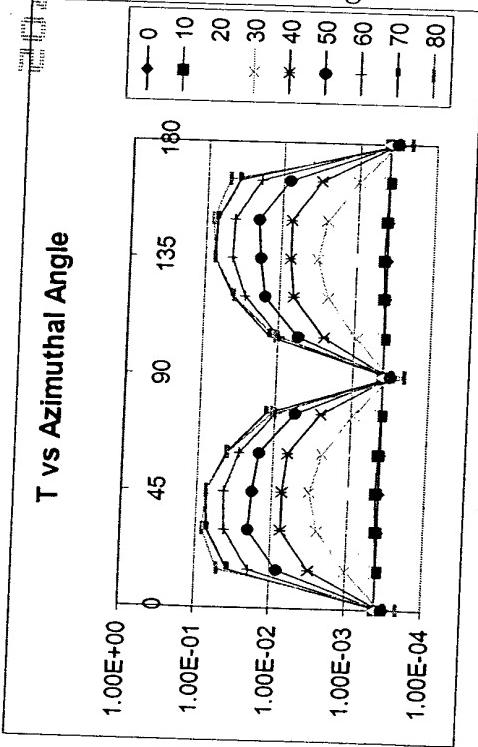


Figure 8a

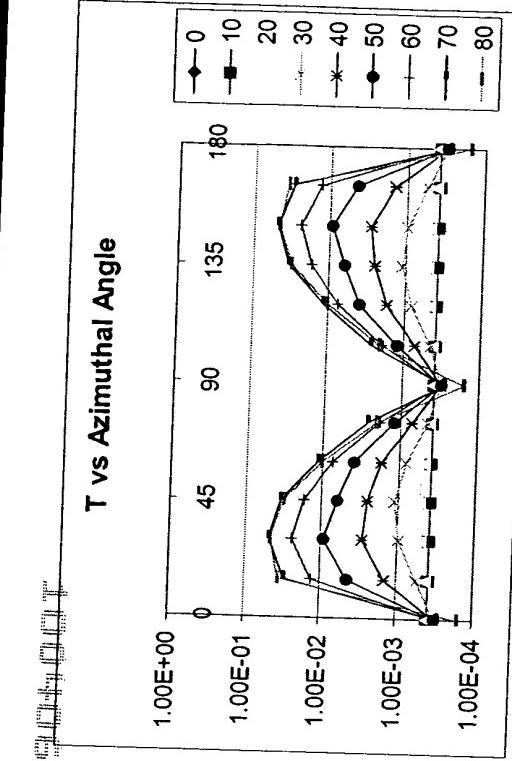


Figure 8c

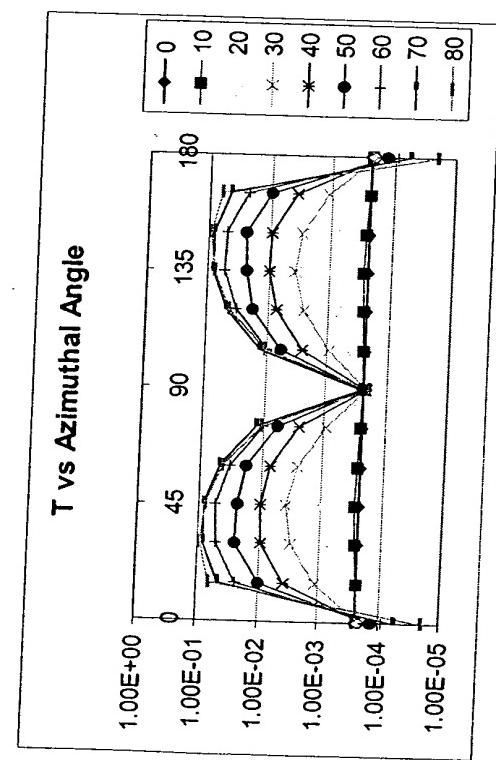


Figure 8b

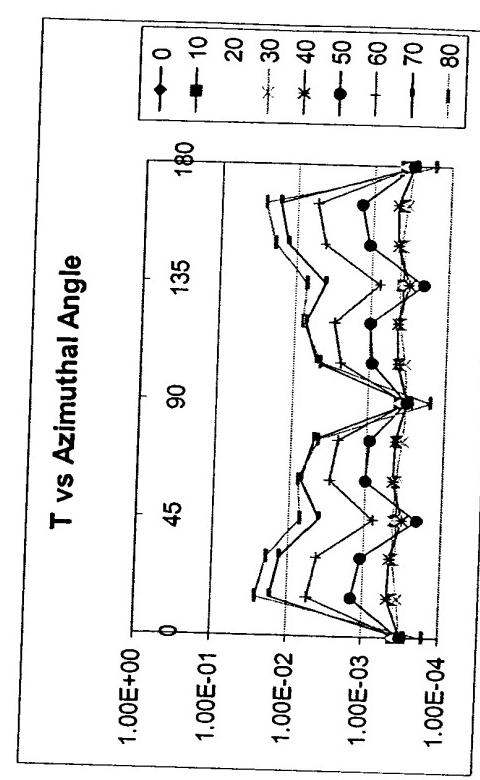


Figure 8d

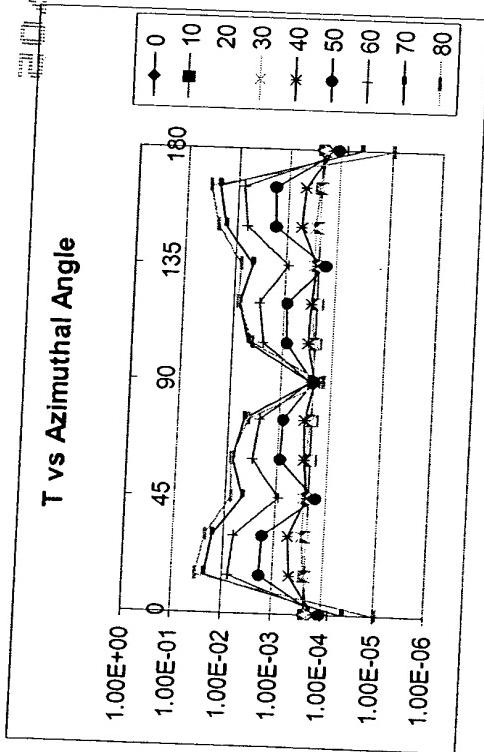


Figure 8e

Figure 8d
Figure 8e
Figure 8f
Figure 8g
Figure 8h

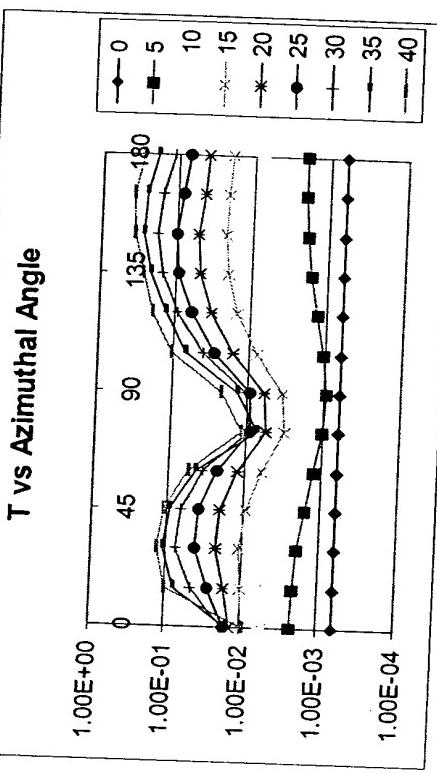


Figure 8g

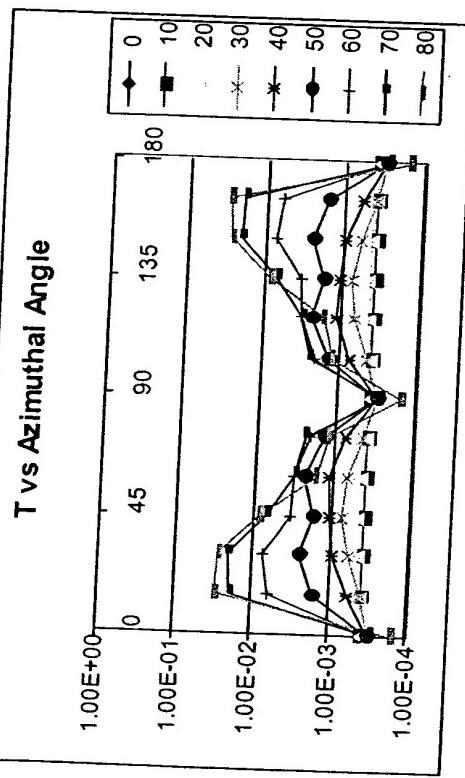


Figure 8f

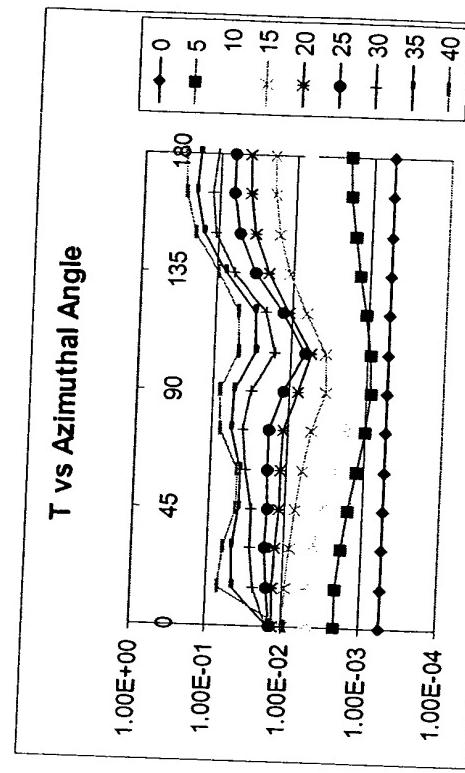


Figure 8h

Without Compensator at 88 degree

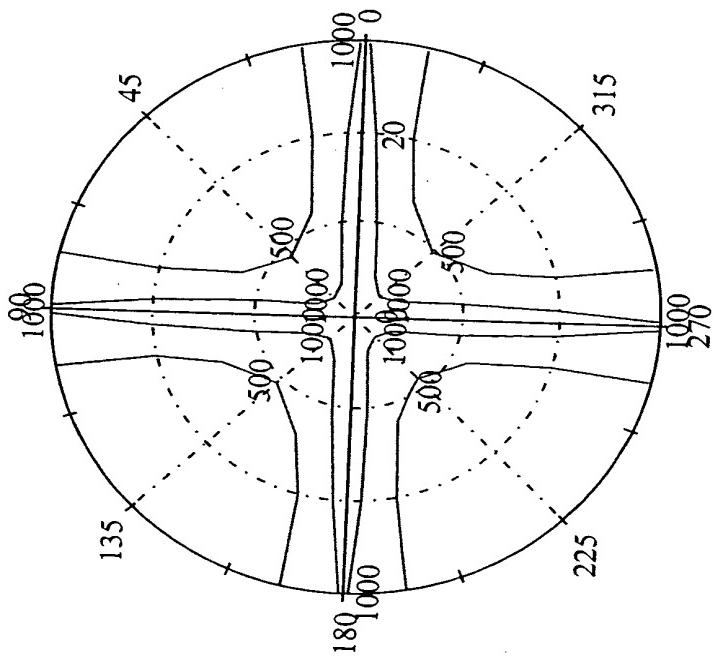
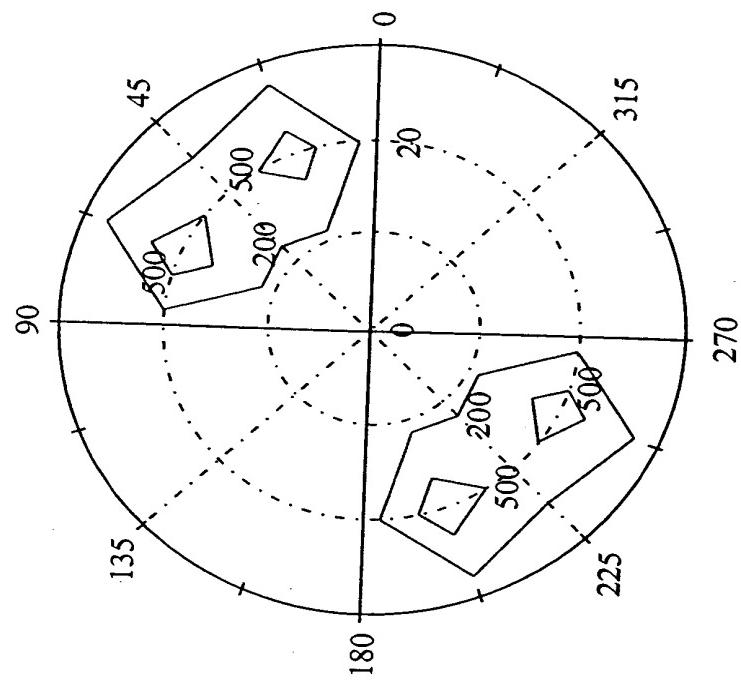


Figure 9a

Figure 9b

10nm A-plate at -45 degree



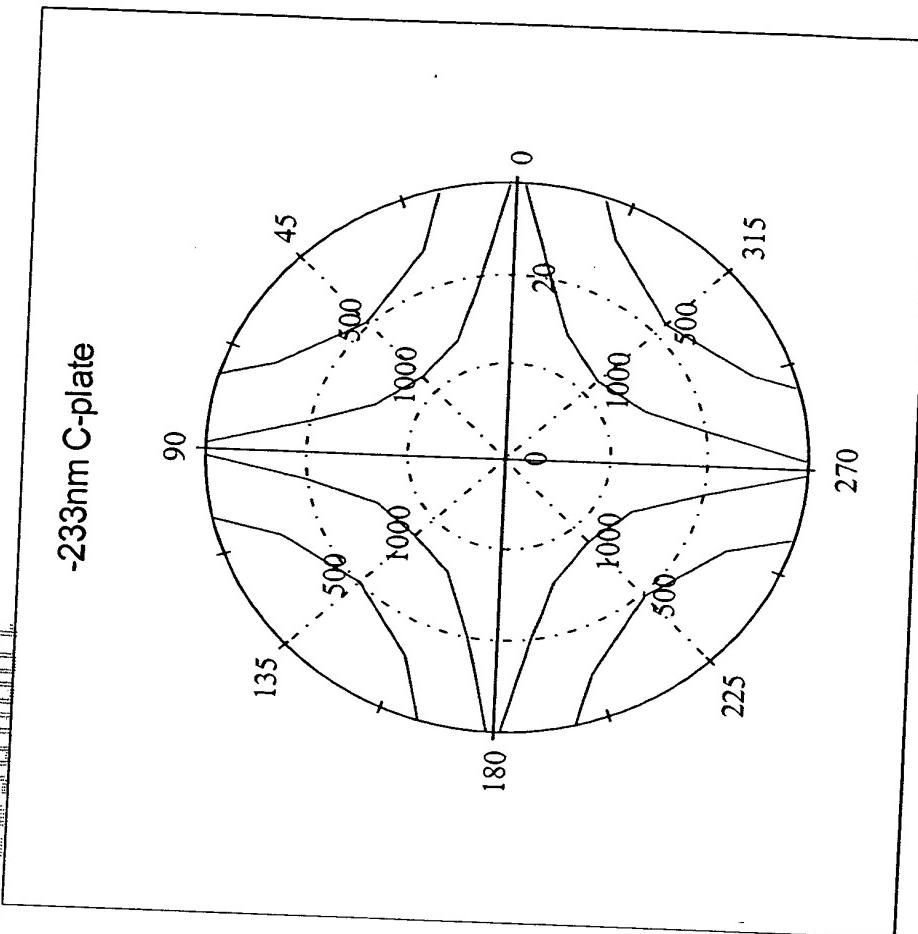


Figure 9c

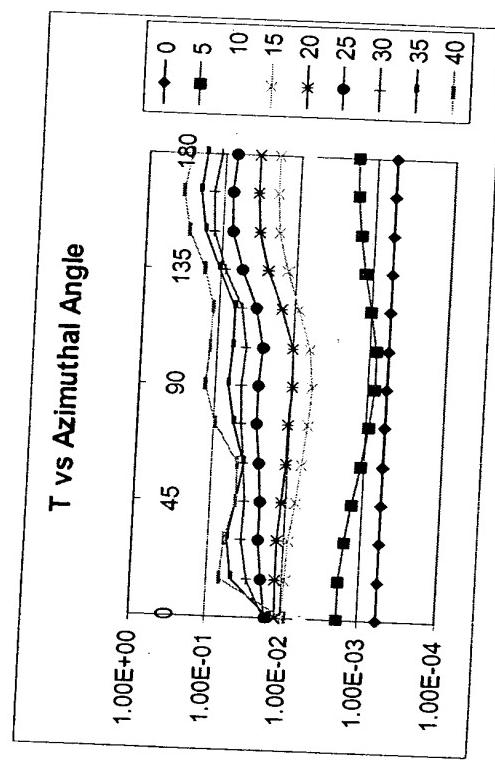


Figure 8i

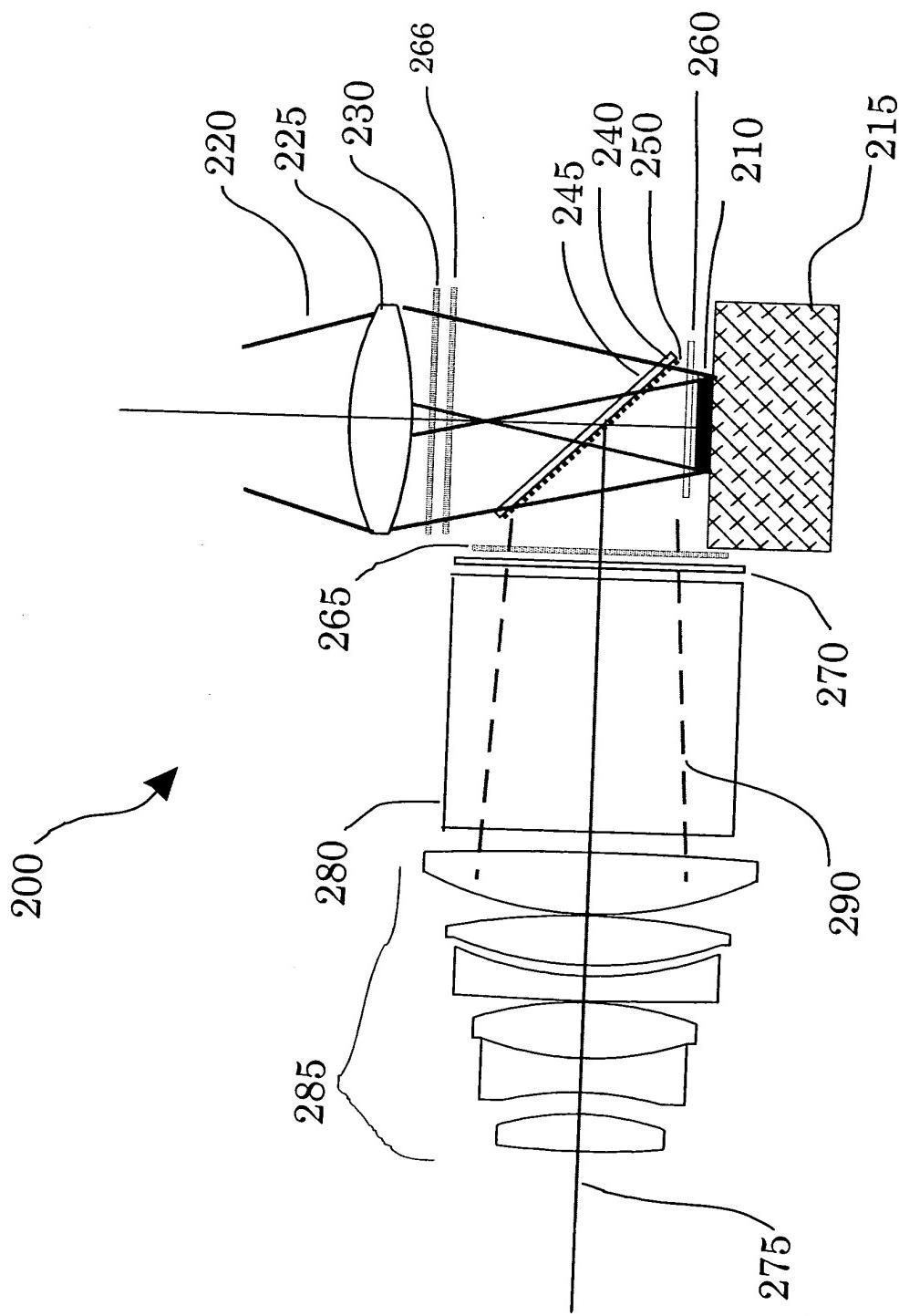


Figure 10